

Session E

(Joint with MMWMC)

Components 2

E

Chairman:

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Development of wideband, high performance MMIC control circuits are essential to cost effective T/R modules. In this session there are two papers which review new developments in phase shifter technology. The first paper describes 30% bandwidth phase shifters with low RMS phase and amplitude error. The second paper presents a novel 180 phase shifter circuit topology which has fewer elements and requires a single control voltage. Finally the last two papers describe logarithmic amplifiers demonstrating good dynamic range, linearity and pulse response together with an increased level of integration.

1:00 p.m.-2:10 p.m., Tuesday, June 2, 1992
Kiva Auditorium